

How the insertion loss of a switch affects the overall system noise figure. Learn Friis formula application, switch placement, and NF budget impact.

The FET switches in the LCAS operate and release slowly (less than 1 msec.) which acts to reduce the generated impulse noise. These switches all operate similarly so graphs are only shown for the line ...

This application report explains how to implement a low noise switch mode power supply solution. It introduces the sources of noise and explain how to minimize them.

Everything works like a charm... on average: I've got the right supply even with a dying battery, but I also get a high frequency noise that comes from the switching converter. How can I ...

High input impedance + Open loop opamp when switch is open - Stability - Can add switches to make sure the opamp is closed loop when holding

Infineon Technologies provides high-performance radio frequency (RF) transistors targeting WLAN LNA applications. Infineon's reliable high-volume RF transistors offer exceptionally low noise figure (NF), ...

Noise modeled by two statistically independent current sources, $I_1(t)$ for the thermal (or shot) noise and $I_2(t)$ for the icker noise, in parallel with the drain current i_d

Improper measurement techniques can results in exaggerated output noise. Exaggerated output noise measurements can result in overly conservative "methods" for fixing it. It is important to know the ...

This article provides an overview of low-noise board-level switched-mode power-supply options for data-acquisition systems which call for low noise power supplies to be able to guarantee ...

Applications in RF technology and metrology, as well as other fields, need extremely low noise supply voltages. This article explains the traditional design approach vs. a novel, highly ...



Low-noise samples from access switches

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