

The Conduant PXIe-8324 Optical FPGA board provides the user with a hardware platform that is able to sustain full-duplex high-bandwidth transfers through its 8-lane Gen3 PXI Express (PXIe) interface ...

You need a clocking system that works well with high-speed optical transceivers. In most cases, this would involve PLL (Phase-Locked Loop) or MMCM (Mixed-Mode Clock Manager) components in the ...

Optical PCIe FPGA boards with 10GbE, 40GbE and OTN data handling capabilities. Optical network analysis and transmission. Xilinx and Altera FPGA platforms.

PMC mezzanine FPGA boards for optical interfaces including 1GbE, 10GbE, SONET, SDH and OTN. Xilinx FPGAs. Customizable firmware.

As a VITA(TM) 57.1 FMC(TM), the Samtec 14 Gbps FireFly(TM) FMC(TM) Module can be used for optical data communication on any FPGA development board supporting high-speed multi-gigabit transceivers.

On-board optical modules continue to gain acceptance in embedded, medical and mil/aero applications. Samtec offers a wide portfolio of FMC(TM), FMC+(TM) and optical module evaluation kits for real-time ...

QSFP28 100G (4x25G) eye diagram, and FMC8 tested GTY (25G) IBERT 31 bit 24-hour without errors. Product Display.

The FMC410 is an optical transceiver module in the FMC form factor that offers ten independent optical transmit and receive links with maximum data rates of either ...

TI 10G optical module SFP+ total solution is a complete demonstrated-working optical transceiver solution targeted for the small form factor pluggable (SFP+). This solution reduces customer design ...

The V6063 provides twelve (12) full duplex optical ports supporting from 1-25G per lane, FPGA fabric resources, ARM processor cores, and AI/ML hard cores. The V6063 can also be used adjacent to ...

This FMC module is supported by ten SFP/SFP+ ports and high-performance low-jitter oscillator with default frequency value of 156.25MHz.. The SFP/SFP+ ports are directly connected to 10 multi ...



FPGA optical port module

Web: <https://www.safireschools.co.za>

